

Fig. 1

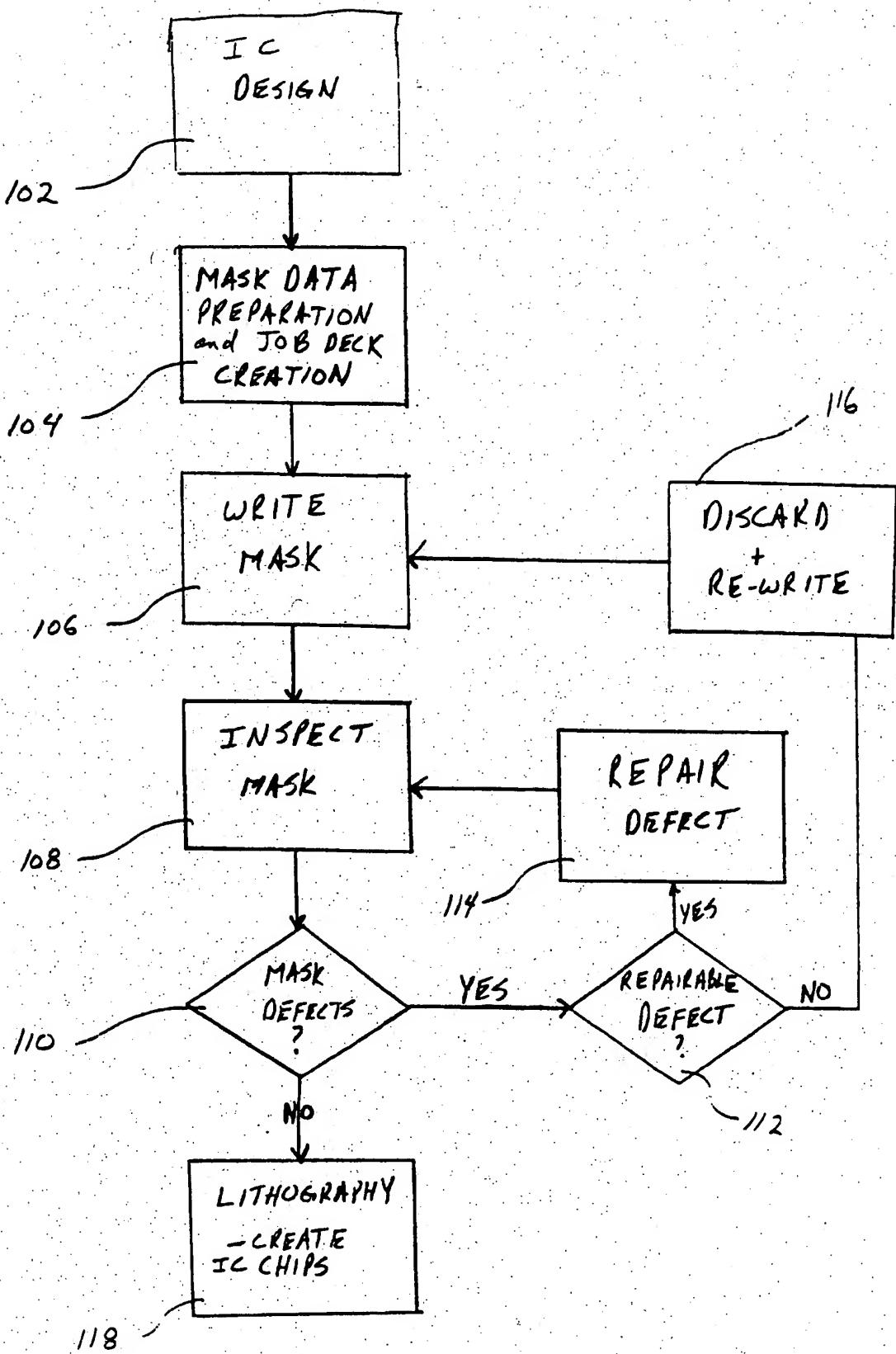


Fig. 2

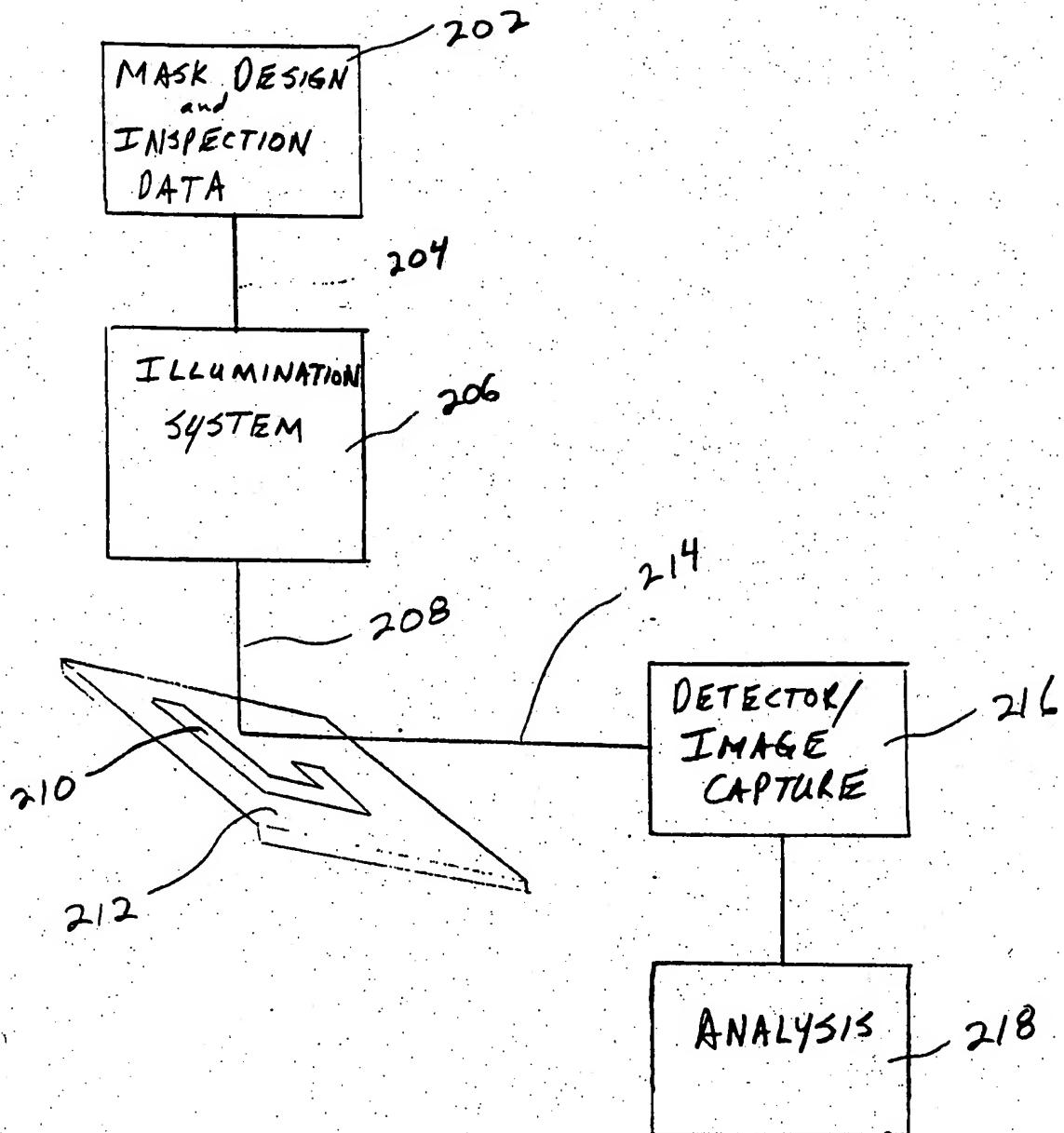


Fig. 3

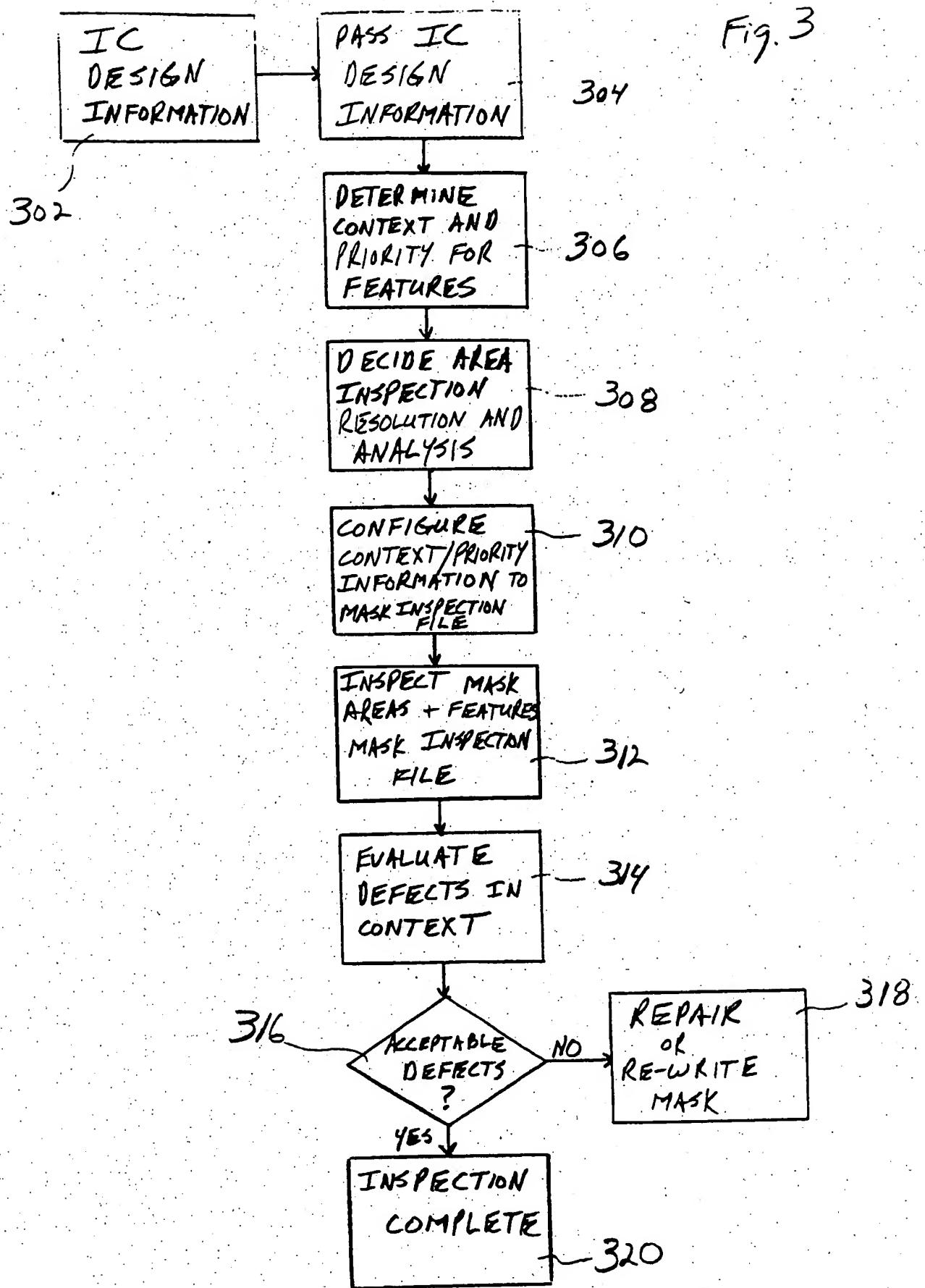


Fig. 4a

FET

402

DEFECT
404



Fig. 4b

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Fig. 4c

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Fig. 4d

416

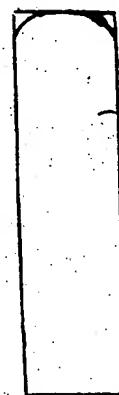


FIG. 5

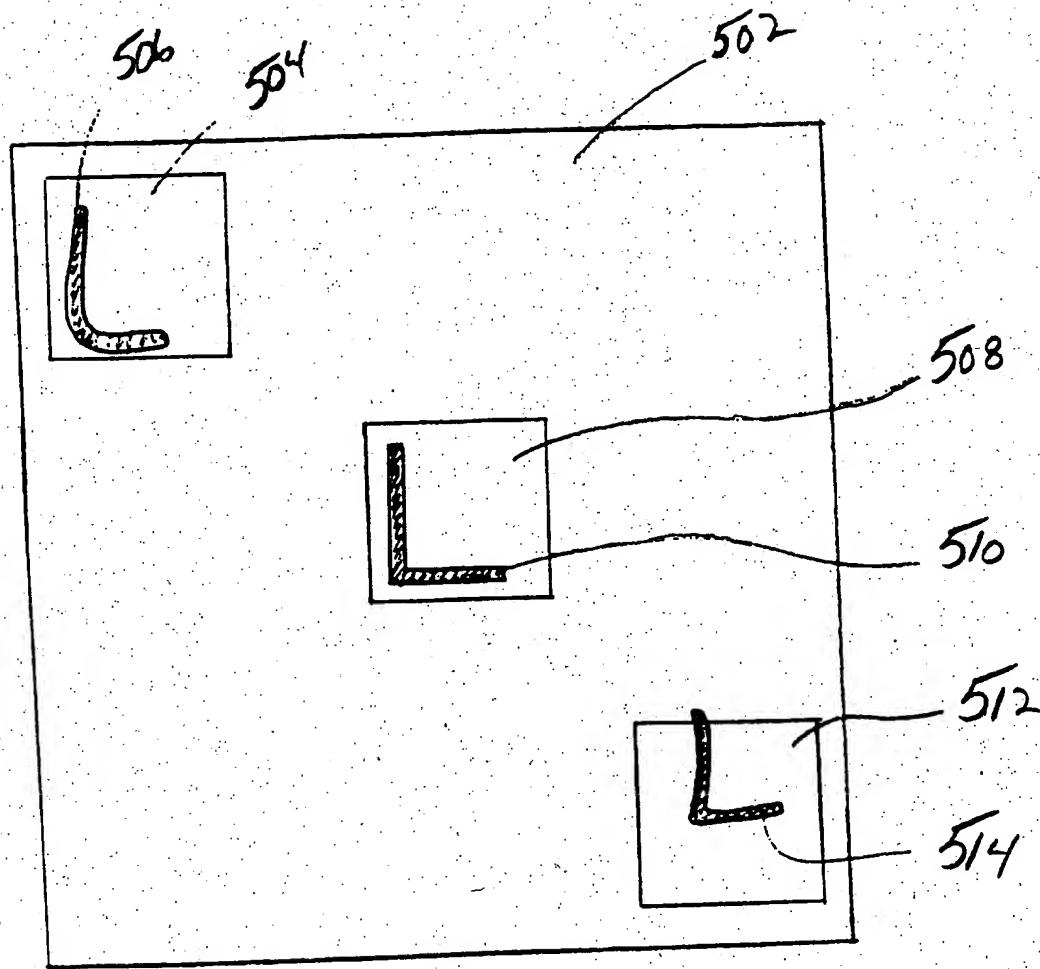


Fig. 6 INTERFACE

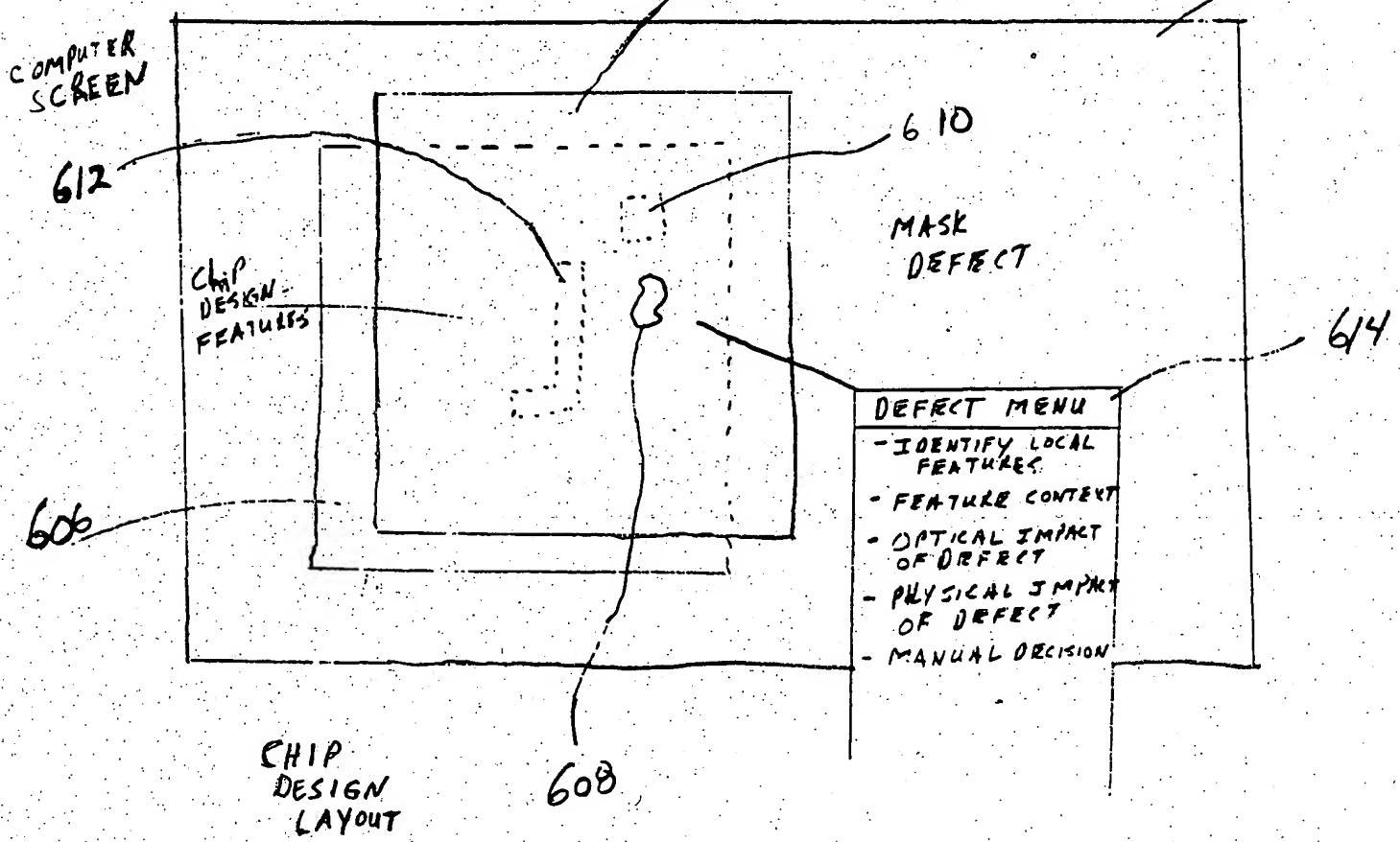


Fig. 7

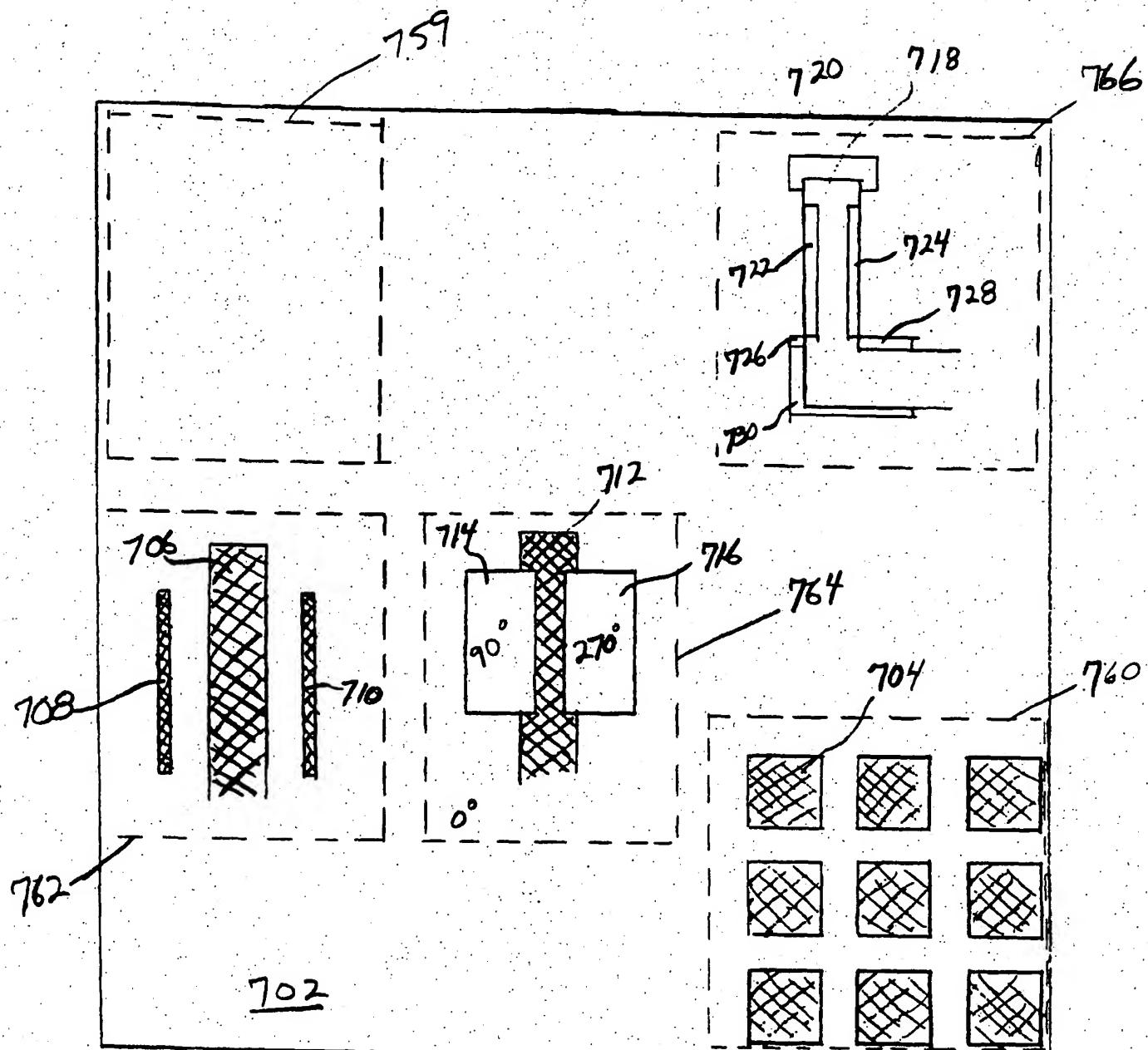


Figure 8A

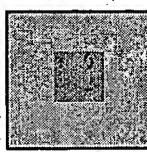


Figure 8B

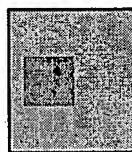


Figure 8C

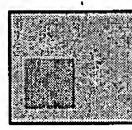


Figure 8D



Figure 8E



Figure 8F

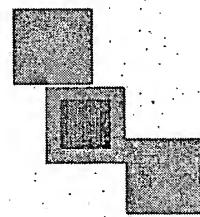
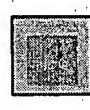


Figure 8G